

Amendments to the Claims:

Please amend claim 26

This listing of claims will replace all prior versions, and listings, of claims in the application.

WHAT IS CLAIMED IS:

Claims 1-17 (Canceled).

18. (Original) A semiconductor integrated circuit device, comprising:
a logic circuit including a logic transistor formed of an insulated gate type field
5 effect transistor as a component thereof and executing a prescribed processing; and
memory circuitry for storing at least data to be used by said logic circuit, said
memory circuitry including a first circuit configured for receiving a first voltage of a
first amplitude and a second circuit configured for receiving a second voltage of a
second amplitude, the second amplitude being greater than the first amplitude,
10 said first circuit including as a component thereof a first-type insulated gate
field effect transistor having a single gate insulting film same at least in thickness as
said logic transistor, said second circuit including as a component thereof a second-type
insulated gate field effect transistor having a single gate insulating film thicker than a
gate insulating film of said logic transistor.

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19. (Original) The semiconductor integrated circuit device according to claim
18, wherein
said memory circuitry further includes:
20 a memory cell array having a plurality of memory cells arranged in rows and
columns, the memory cell including said second-type insulated gate field effect
transistor as a component thereof.

20. (Currently Amended) The semiconductor integrated circuit device
25. according to claim 19, wherein
 said memory circuitry further includes:
 ~~a memory cell array having a plurality of memory cells arranged in rows and~~
 ~~columns; and~~
 a plurality of sense amplifier circuits provided corresponding to the columns of
30 the memory cells, each for sensing and amplifying data of a memory cell on a
 corresponding column,
 the sense amplifier circuit including said first-type insulated gate field effect
 transistor as a component thereof.

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